Electrical Circuits

Circuit analysis: Node and mesh analysis, superposition, Thevenin's theorem, Norton's theorem, reciprocity.

Sinusoidal steady state analysis: phasors, complex power, maximum power transfer. Time and frequency domain analysis of linear circuits: RL, RC and RLC circuits, solution of network equations using Laplace transform.

Linear 2-port network parameters, wye-delta transformation.

Electronic Devices

Energy bands in intrinsic and extrinsic semiconductors, equilibrium carrier concentration, direct and indirect band-gap semiconductors.

Carrier transport: diffusion current, drift current, mobility and resistivity, generation and recombination of carriers, Poisson and continuity equations.

P-N junction, Zener diode, BJT, MOS capacitor, MOSFET, LED, photo diode and solar cell.

Analog Circuits

Diode circuits: clipping, clamping and rectifiers.

BJT and MOSFET amplifiers: biasing, ac coupling, small signal analysis, frequency response. Current mirrors and differential amplifiers.

Op-amp circuits: Amplifiers, summers, differentiators, integrators, active filters, Schmitt triggers and oscillators.

Digital Circuits

Number representations: binary, integer and floating-point- numbers.

Combinatorial circuits: Boolean algebra, minimization of functions using Boolean identities and Karnaugh map, logic gates and their static CMOS implementations,

arithmetic circuits, code converters, multiplexers, decoders.

Sequential circuits: latches and flip-flops, counters, shift-registers, finite state machines, propagation delay, setup and hold time, critical path delay.

Data converters: sample and hold circuits, ADCs and DACs.

Semiconductor memories: ROM, SRAM, DRAM.

Programming and Computer Architecture

Computer organization: Machine instructions and addressing modes, ALU, data-path and control unit, instruction pipelining.

C Programming: Data types, pointers, arrays, loops and conditional statements, functions, Data structures: linked lists, stacks, queues, trees, graphs.

Algorithms: searching, sorting, graph traversal.